

FIG. 1

Reader Network / Site

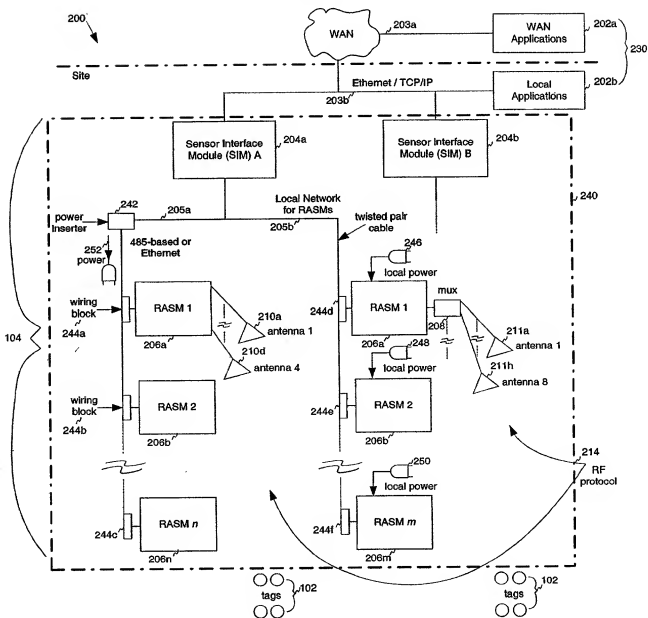
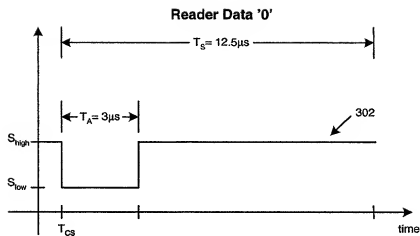
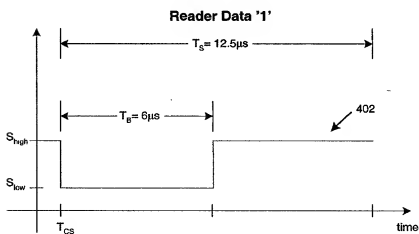
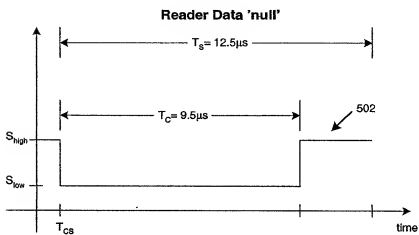


FIG. 2

**FIG. 3****FIG. 4****FIG. 5**

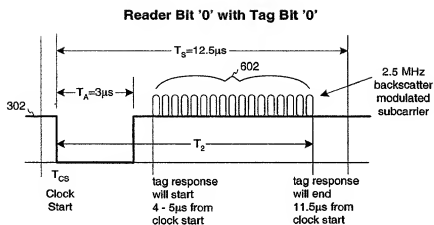


FIG. 6

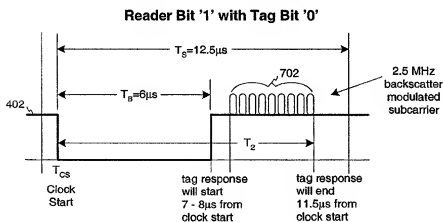


FIG. 7

Reader Bit '0' with Tag Bit '1'

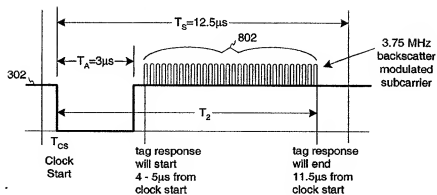


FIG. 8

Reader Bit '1' with Tag Bit '1'

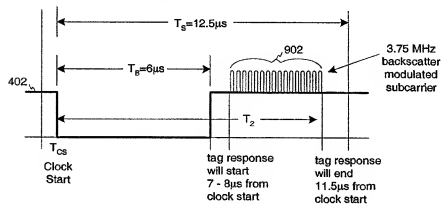


FIG. 9

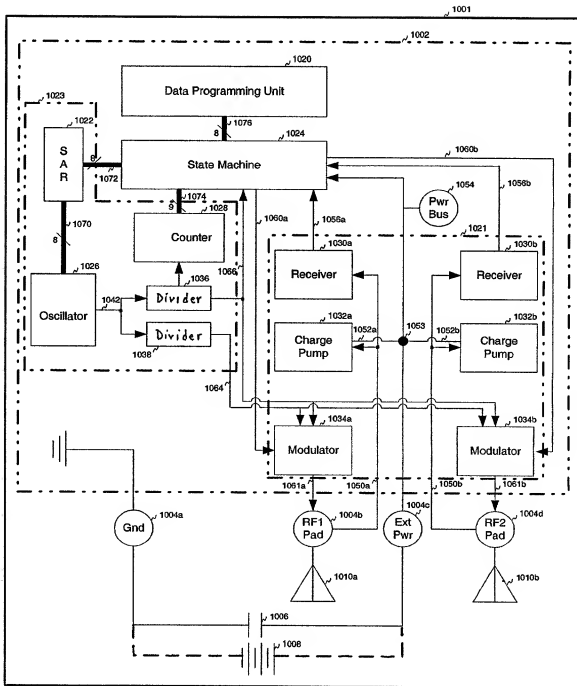


FIG. 10

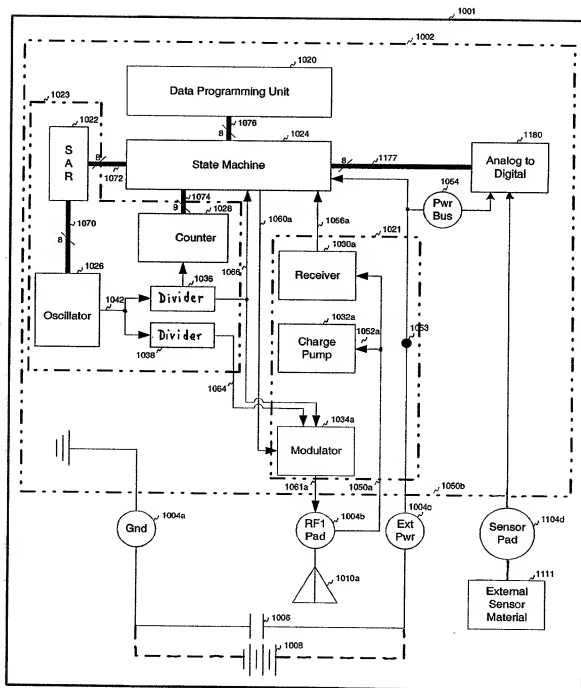


FIG. 11

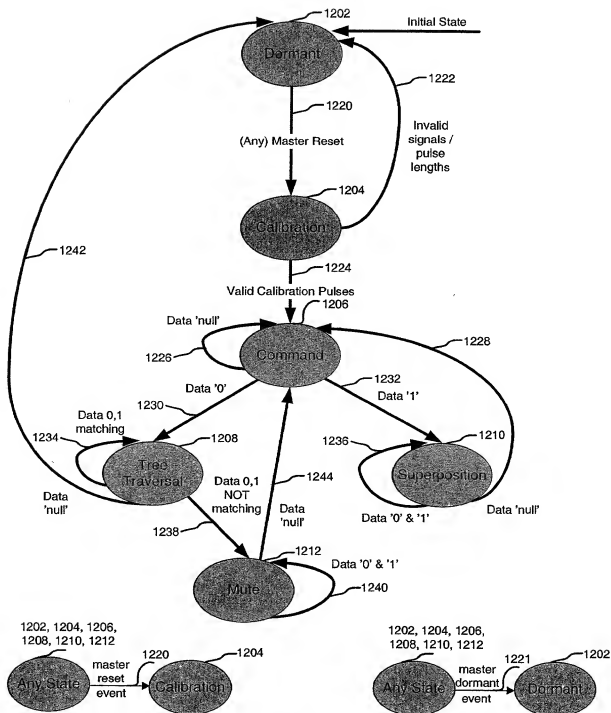


FIG. 12A

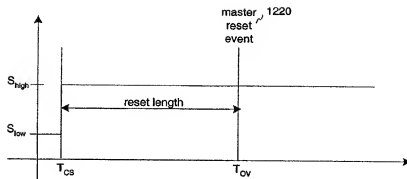


FIG. 12B

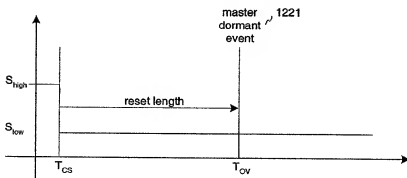


FIG. 12C

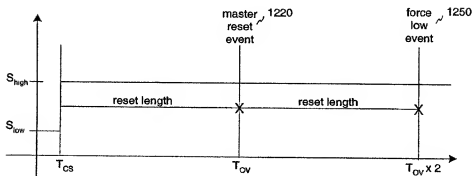


FIG. 12D

Binary Traversal Flowchart from a Tag Perspective

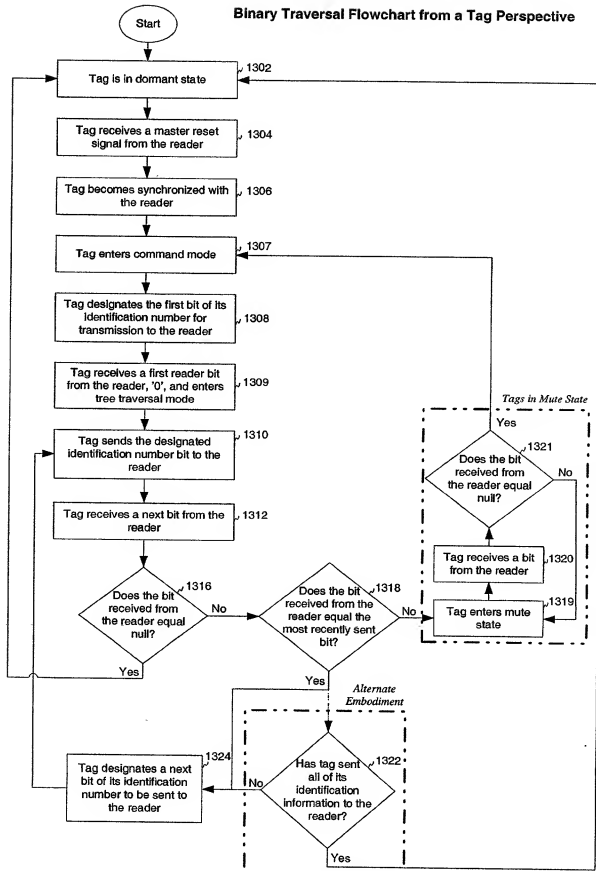


FIG.13

Read Specific Tag, Reader Perspective

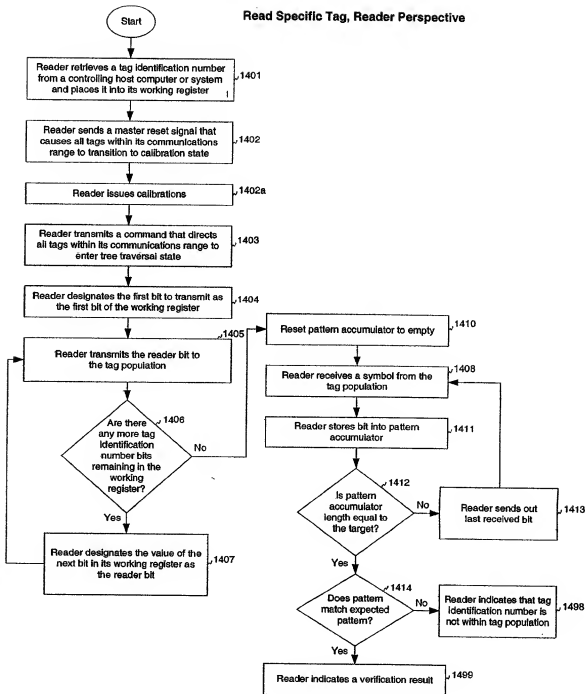


FIG. 14A

Read Specific Tag, Alt., Reader Perspective

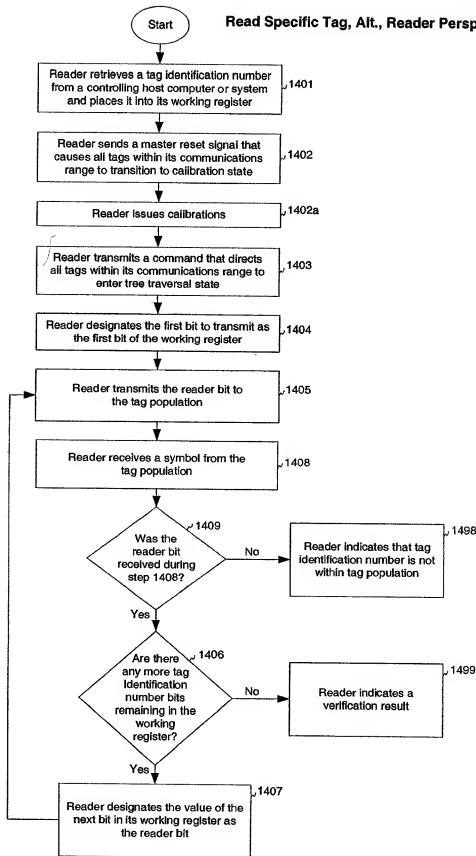


FIG. 14B

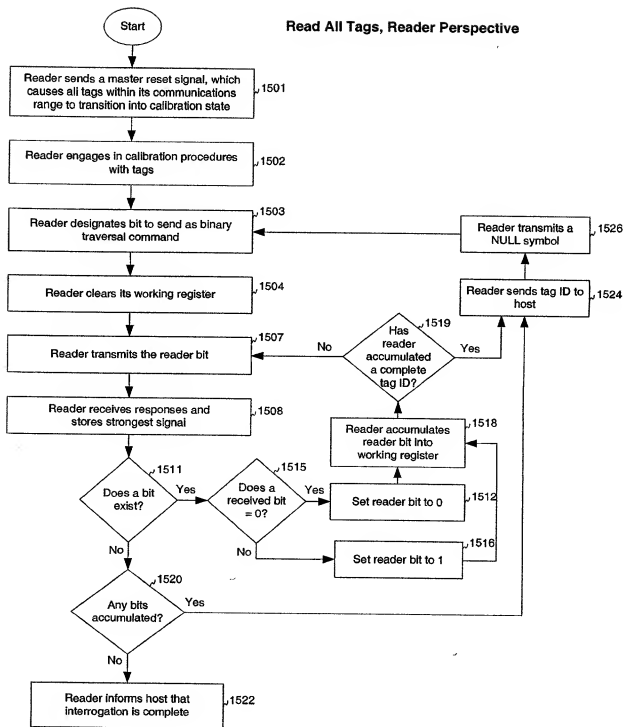


FIG. 15A :

Read All Tags, Alt., Reader Perspective

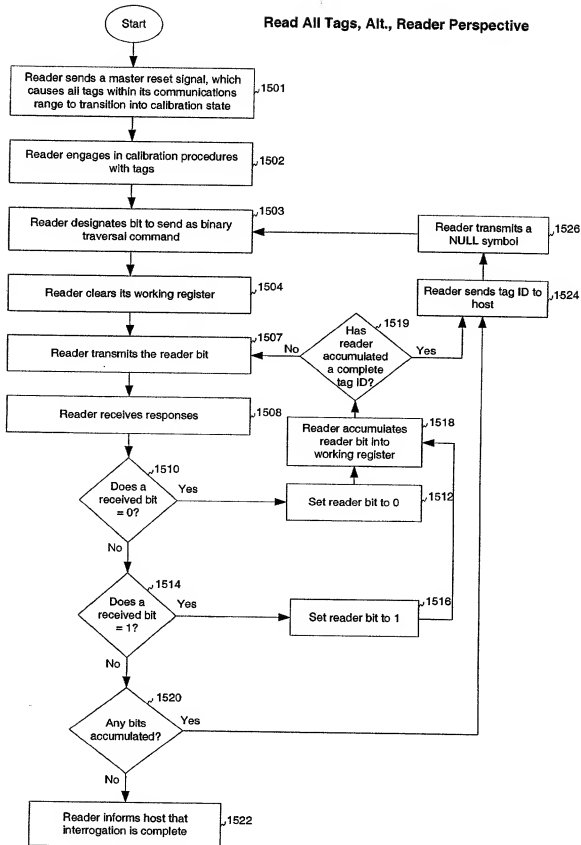


FIG. 15B

Binary Traversal Paths and Tree for a 3-bit Tag Population

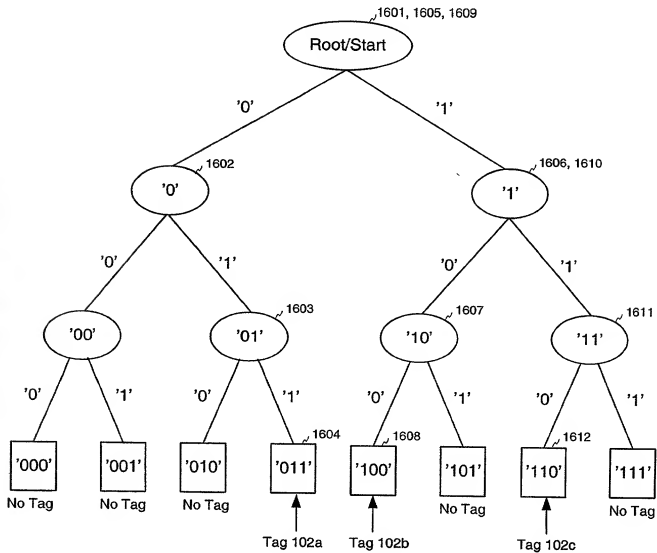


FIG. 16

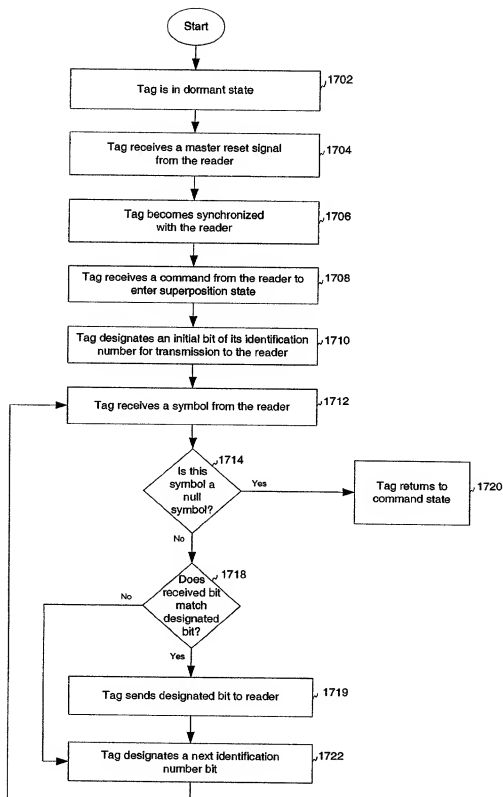


FIG. 17A

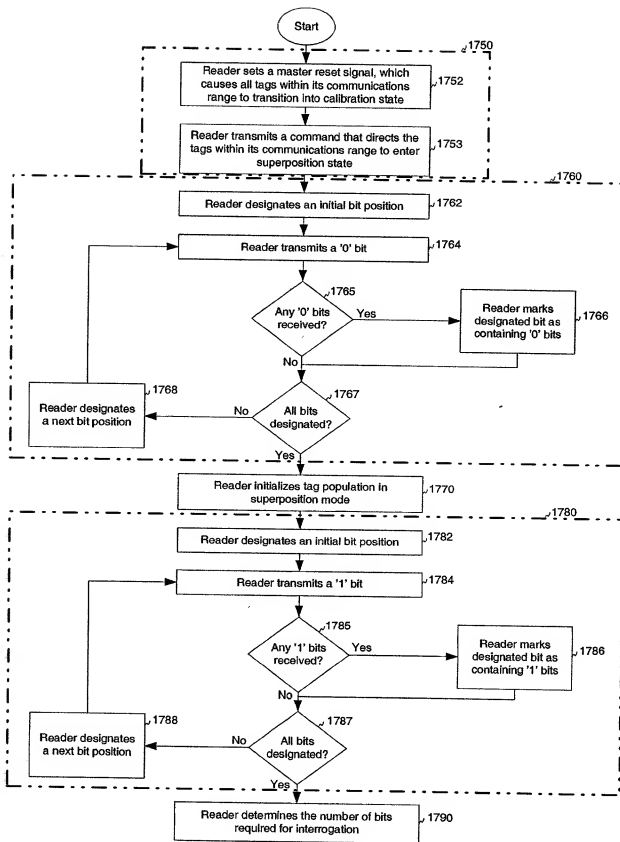


FIG. 17B

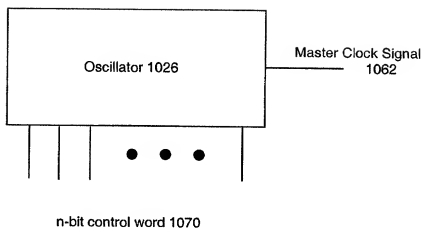


FIG. 18

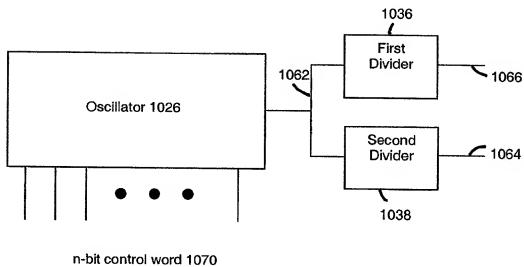


FIG. 19

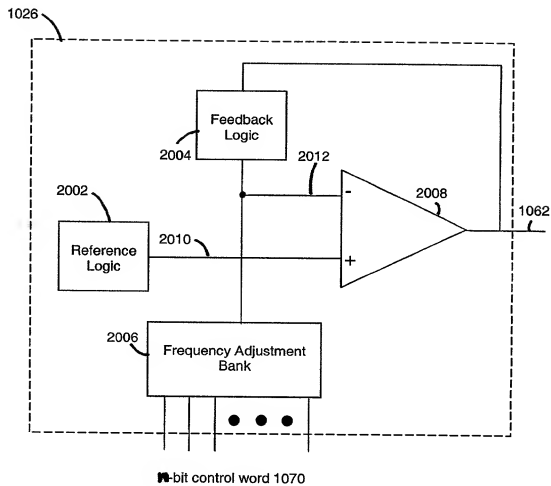


FIG. 20

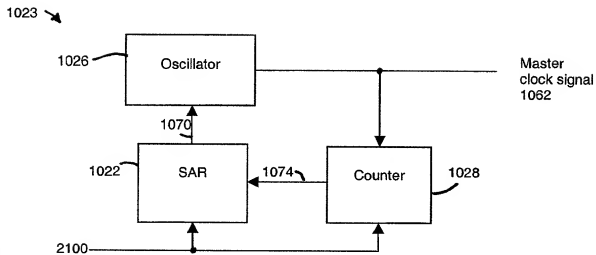


FIG. 21A

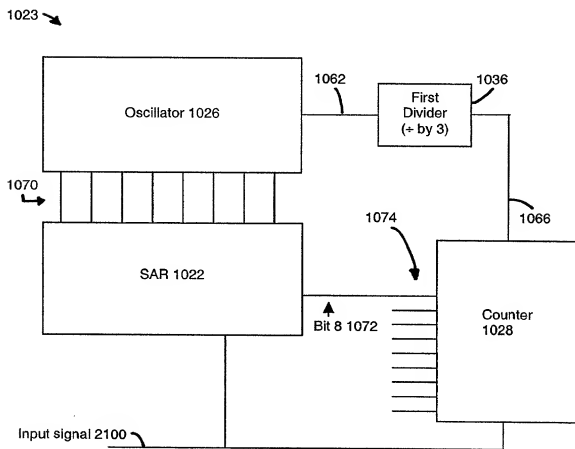


FIG. 21B

Conceptual implementation of SAR calibration

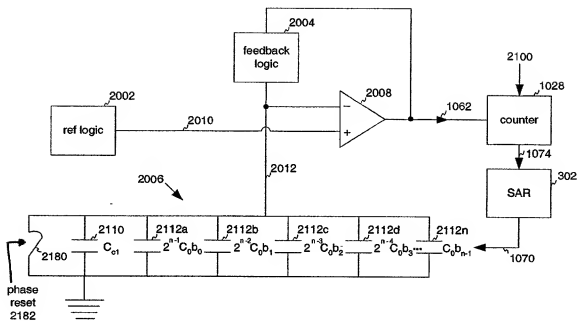


FIG. 21C

Physical implementation of adjustable capacitor bank

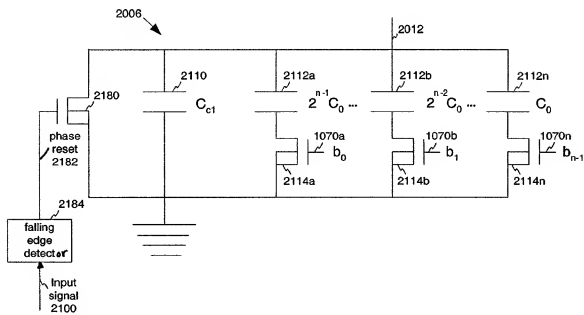
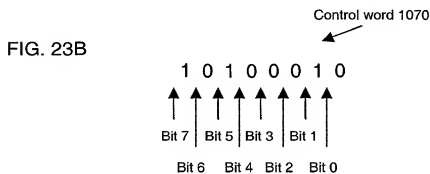
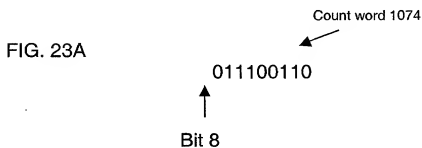
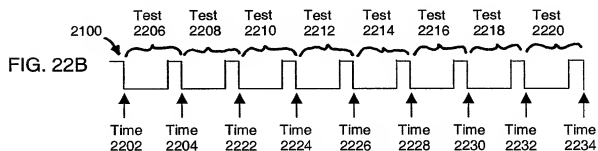
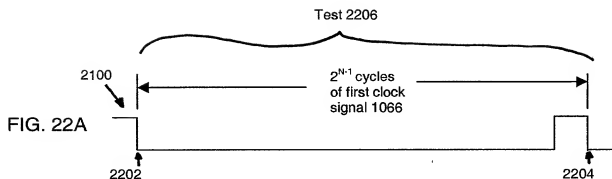


FIG. 21D



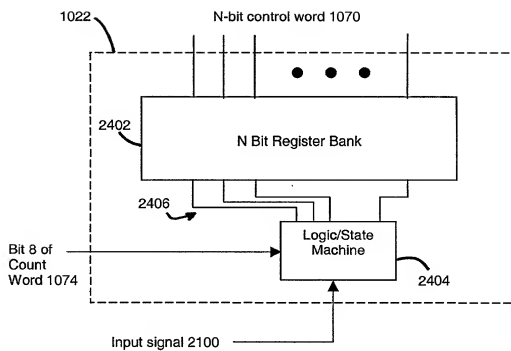
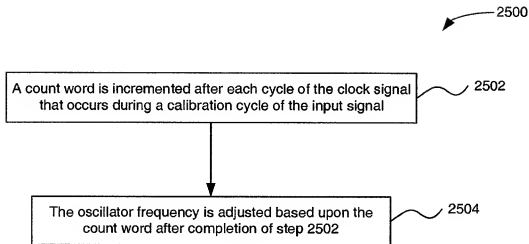
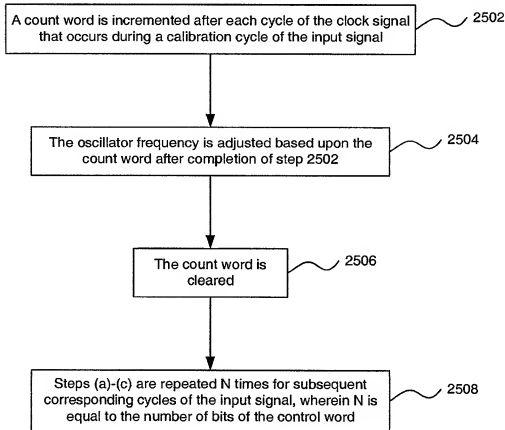
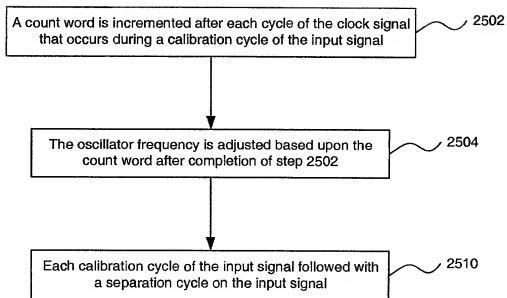


FIG. 24

**FIG. 25A****FIG. 25B**

**FIG. 25C**

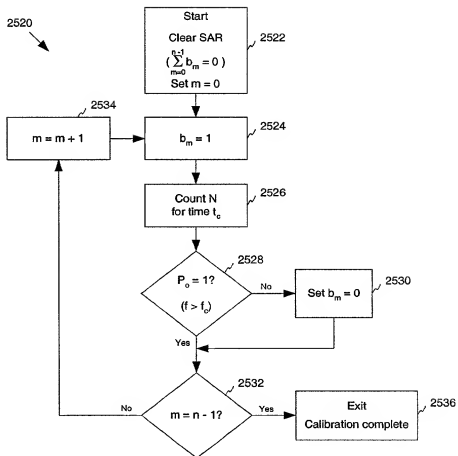


FIG. 25D

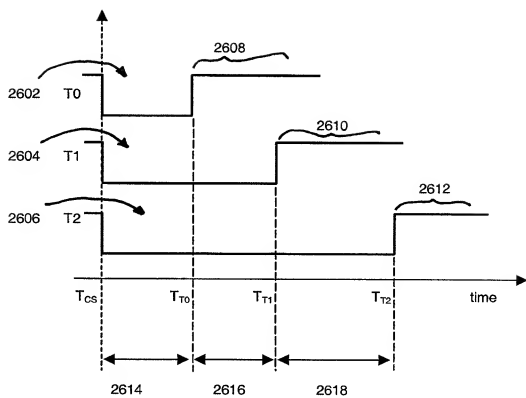


FIG. 26A

10072685-021202

Data Symbol Timing Chart

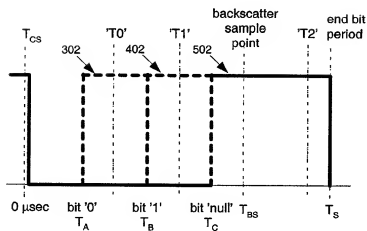


FIG. 26B

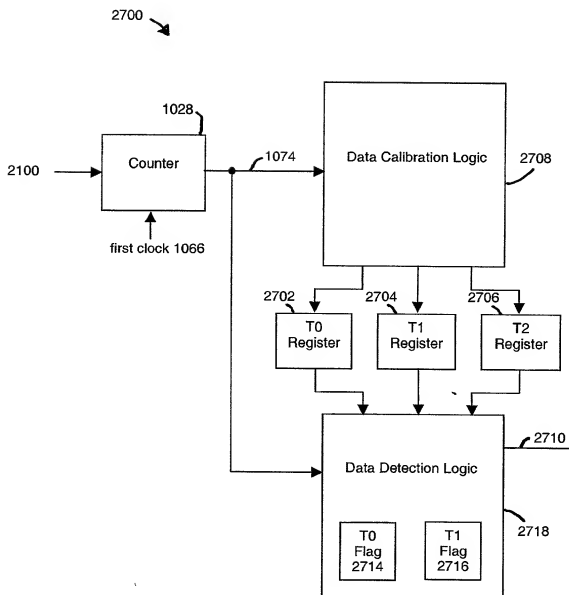


FIG. 27

10072885.021202

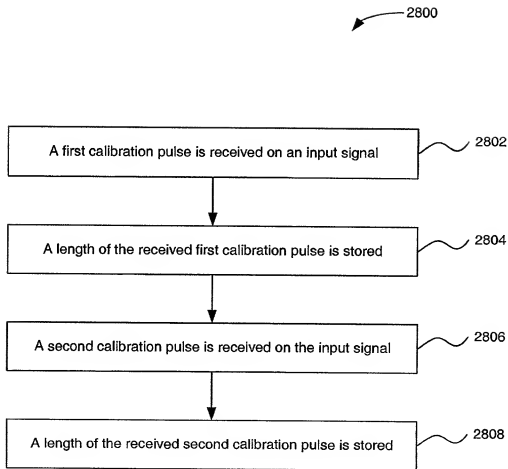
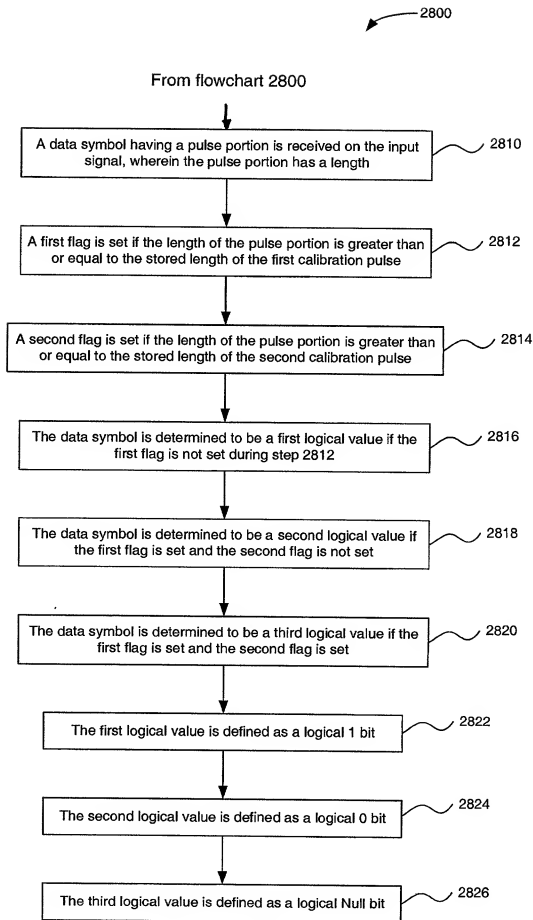
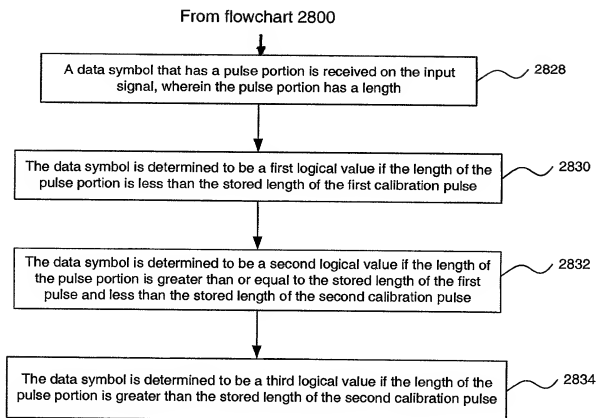


FIG. 28A

**FIG. 28B**

**FIG. 28C**

202120158827001

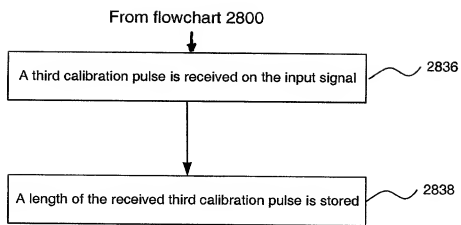


FIG. 28D

2800

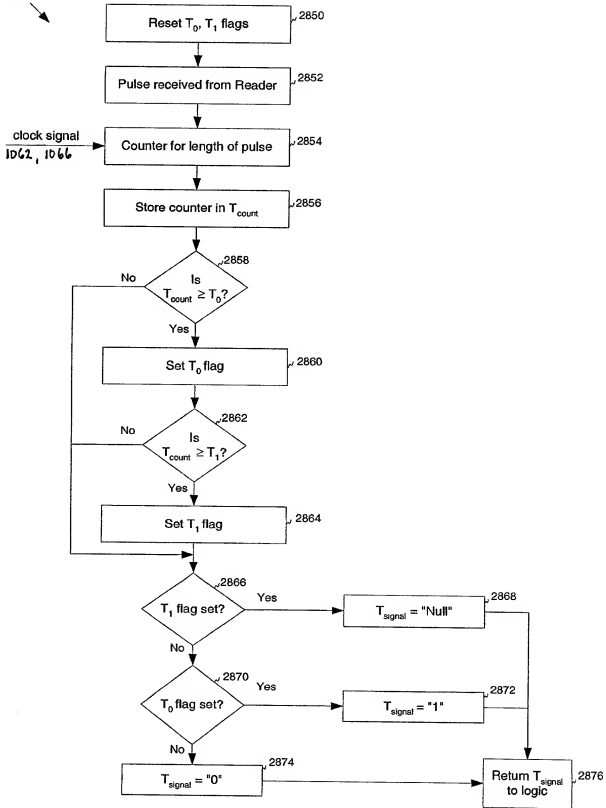


FIG. 28E

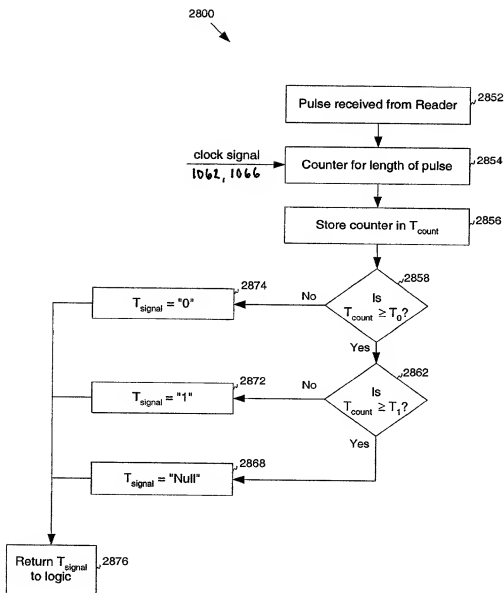
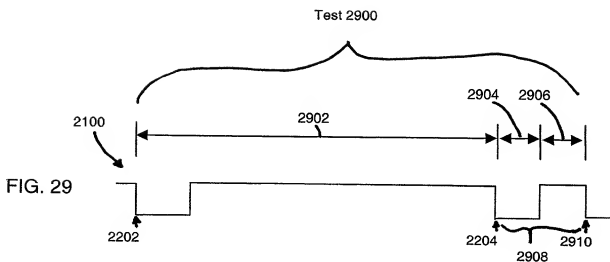


FIG. 28F



Analog Front End

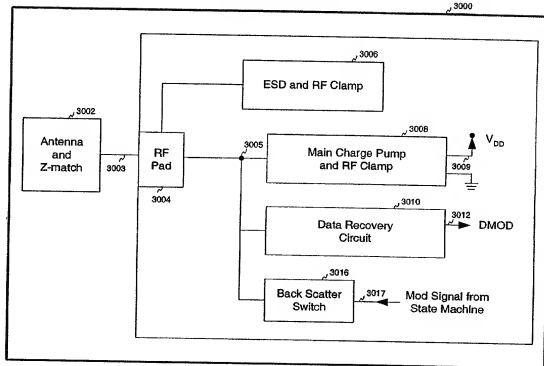


FIG. 30

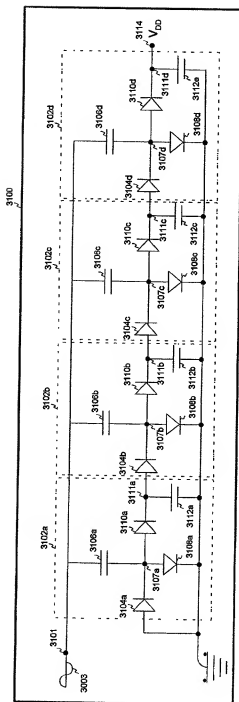


FIG. 31

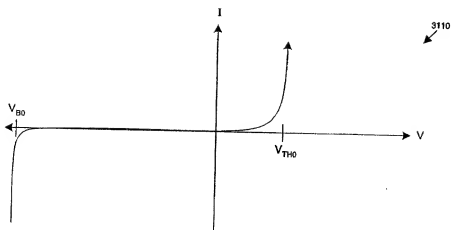


FIG. 32A

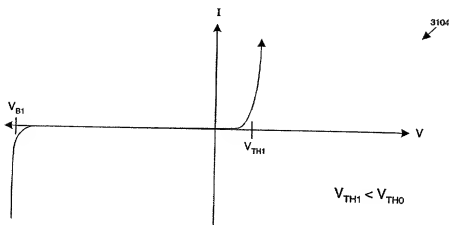


FIG. 32B

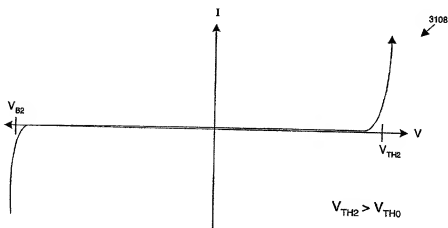


FIG. 32C

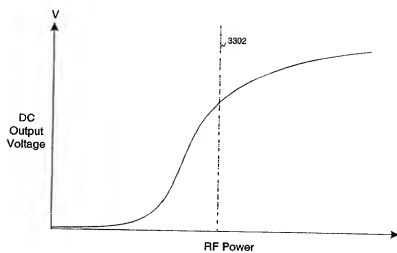


FIG. 33A

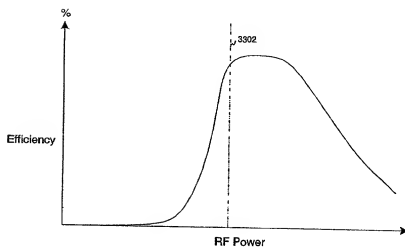


FIG 33B

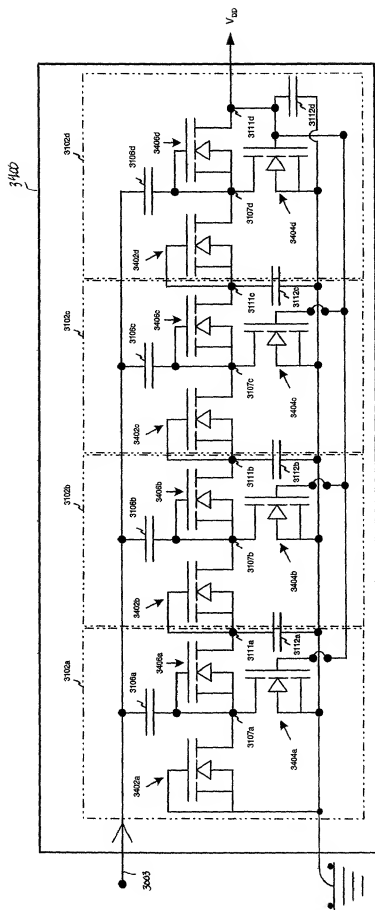


FIG. 34A

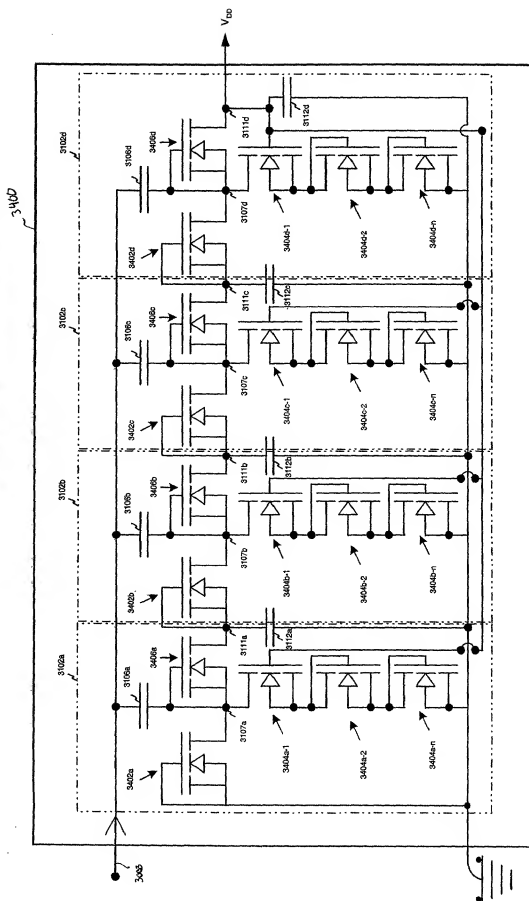


FIG. 34B

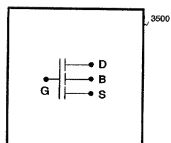


FIG. 35A

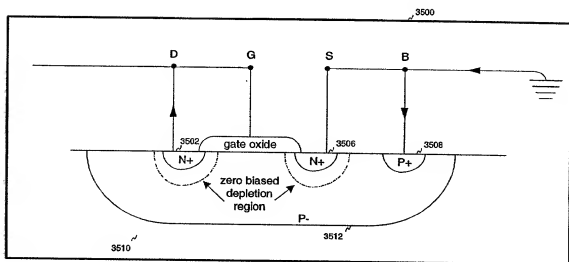


FIG. 35B

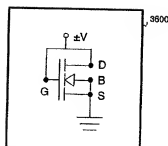


FIG. 36A

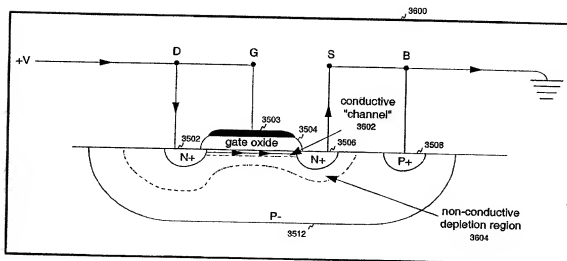


FIG. 36B

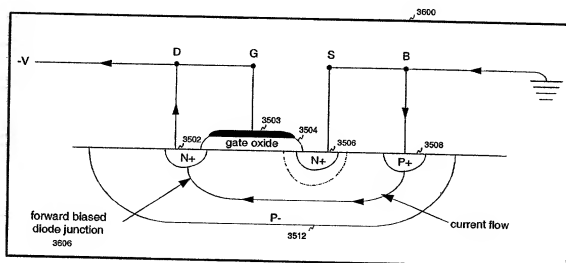


FIG. 36C

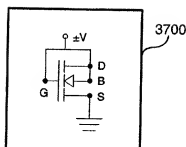


FIG. 37A

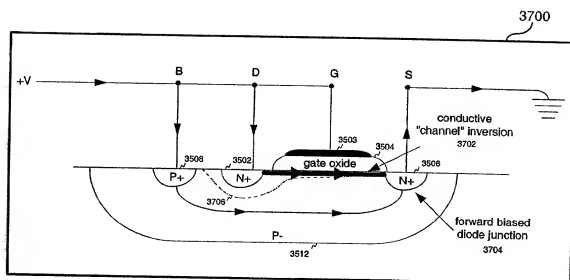


FIG. 37B

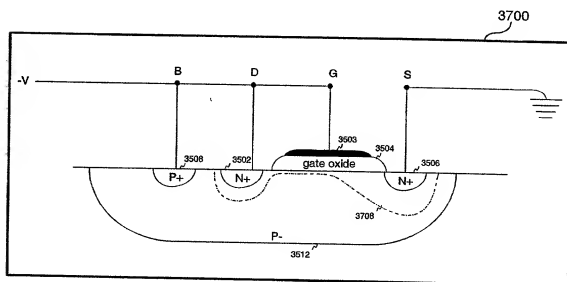
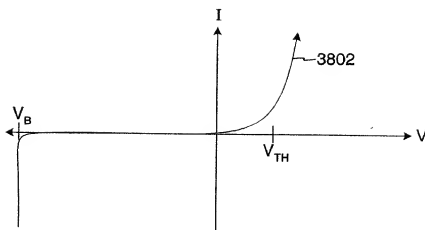
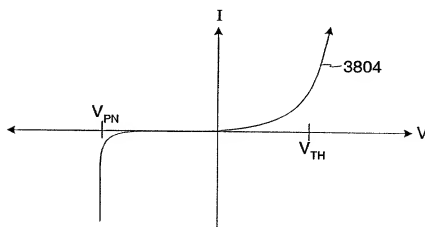
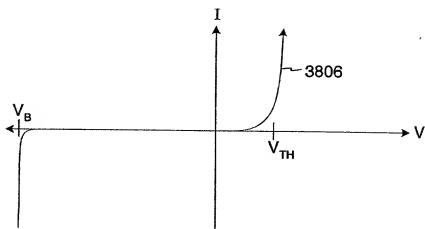


FIG. 37C

**FIG. 38A****FIG. 38B****FIG. 38C**

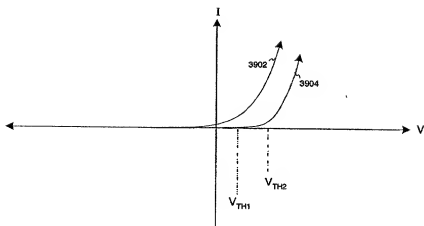


FIG. 39

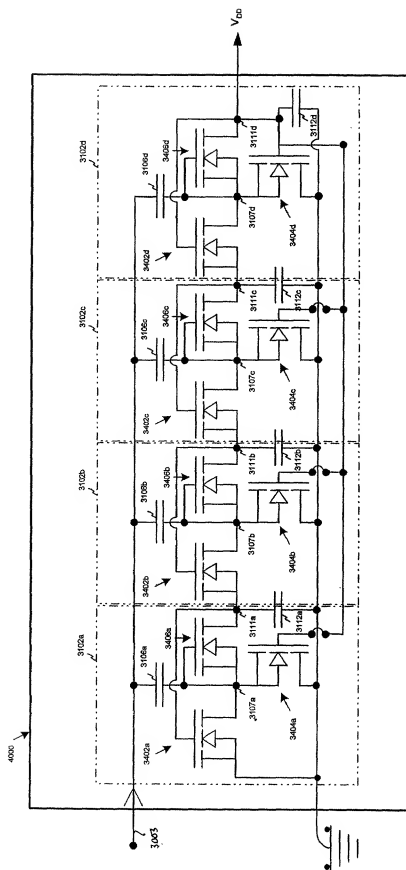


FIG. 40A

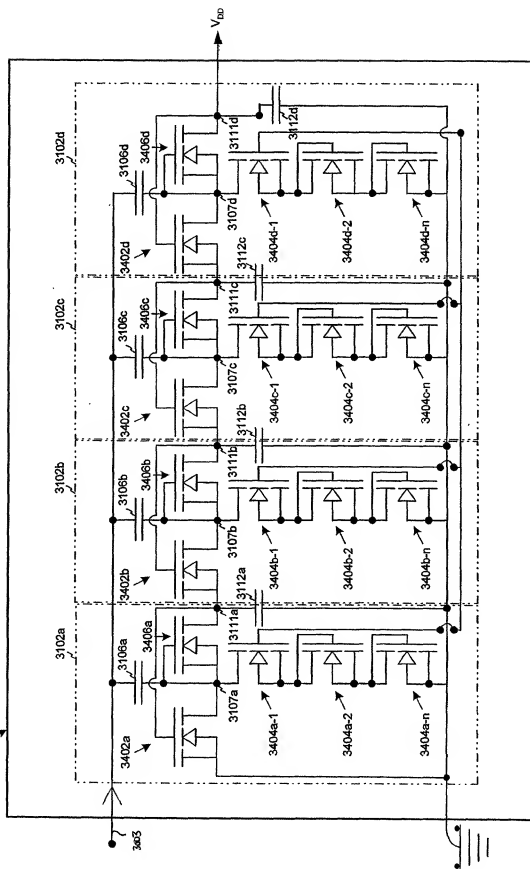


FIG. 40B

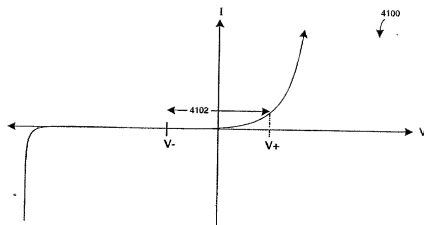


FIG. 41A

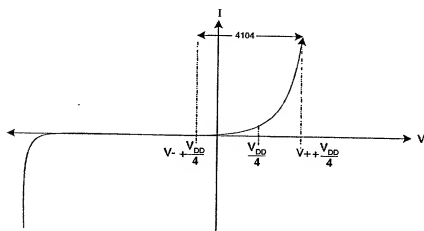


FIG. 41B

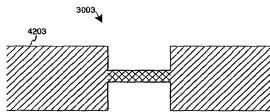


FIG. 43A

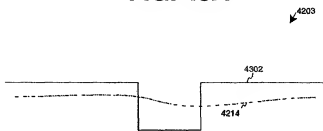


FIG. 43B

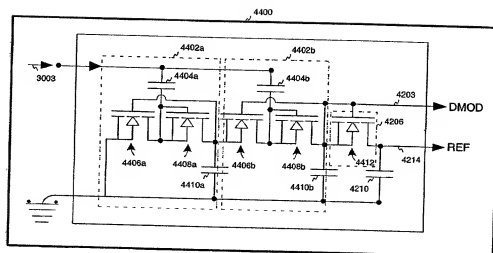


FIG. 44

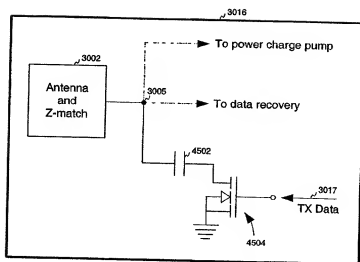


FIG. 45